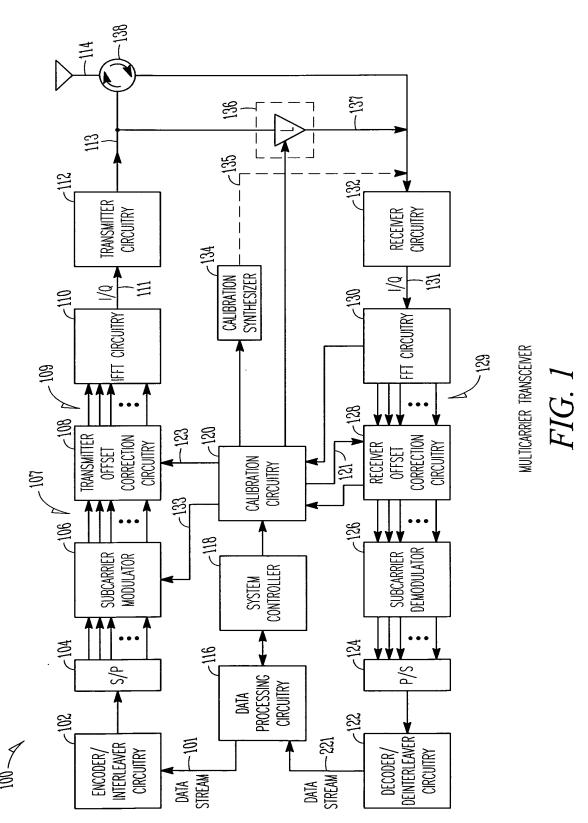


TRANSCEIVER WITH CALIBRATED I AND Q PATHS AND METHODS FOR DECONVOLVED CALIBRATION INVENTOR'S NAME: GEORGIOS PALASKAS, ET AL.

SERIAL NO.: 10/815,138 DOCKET NO.: 884.B88US1

REPLACEMENT SHEET

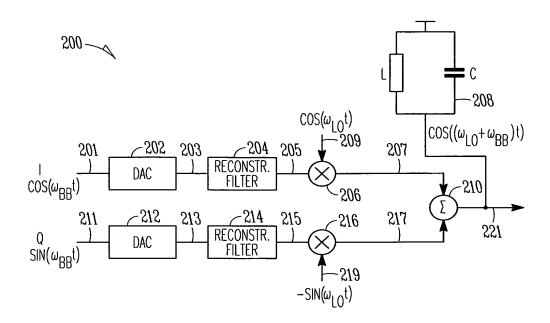
1/5



SERIAL NO.: 10/815,138 DOCKET NO.: 884.B88US1

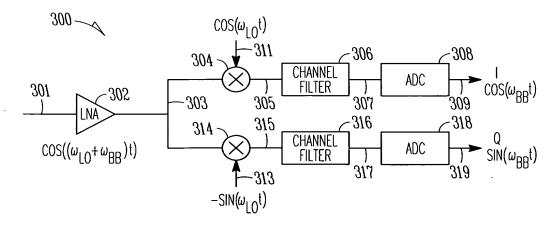
REPLACEMENT SHEET

2/5



TRANSMITTER CIRCUITRY

FIG. 2



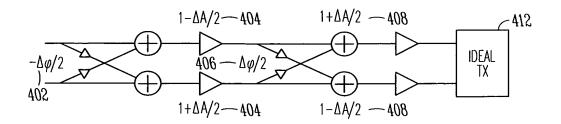
RECEIVER CIRCUITRY

FIG. 3

TITLE: TRANSCEIVER WITH CALIBRATED I AND Q PATHS AND METHODS FOR DECONVOLVED CALIBRATION INVENTOR'S NAME: GEORGIOS PALASKAS ET AL

INVENTOR'S NAME: GEORGIOS PALASKAS, ET AL.
SERIAL NO.: 10/815,138 DOCKET NO.: 884.B88US1 REPLACEMENT SHEET

*3/5* 



TIME-DOMAIN CORRECTION

FIG. 4

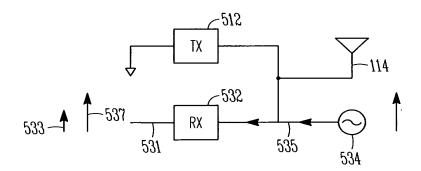


FIG. 5

TITLE: TRANSCEIVER WITH CALIBRATED I AND Q PATHS AND METHODS FOR DECONVOLVED CALIBRATION INVENTOR'S NAME: GEORGIOS PALASKAS, ET AL.

SERIAL NO.: 10/815,138 DOCKET NO.: 884.B88US1

REPLACEMENT SHEET

4/5

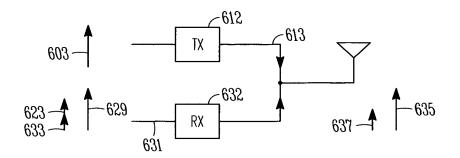


FIG. 6

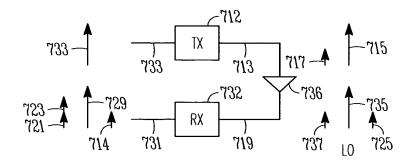


FIG. 7

TITLE: TRANSCEIVER WITH CALIBRATED I AND Q PATHS AND METHODS FOR DECONVOLVED CALIBRATION INVENTOR'S NAME: GEORGIOS PALASKAS, ET AL.

SERIAL NO.: 10/815,138 DOCKET NO.: 884.B88US1

REPLACEMENT SHEET

5/5

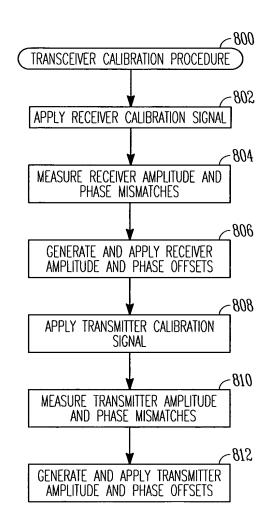


FIG. 8